





# Seagate® FireCuda® 530R SSD

# **Product Manual**

<b>User Capacity</b>	Standard Models	<b>Heatsink Models</b>
1000GB	ZP1000GM30063	ZP1000GM30073
2000GB	ZP2000GM30063	ZP2000GM30073
4000GB	ZP4000GM30063	ZP4000GM30073

#### **Revision History**

Version and Date	Description of Changes
May 2024	First document release.

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When referring to drive capacity, one gigabyte, or GB, equals one billion bytes and one terabyte, or TB, equals one trillion bytes. Your computer's operating system may use a different standard of measurement and report a lower capacity. In addition, some of the listed capacity is used for formatting and other functions, and thus will not be available for data storage. Actual quantities will vary based on various factors, including file size, file format, features and application software. Actual data rates may vary depending on operating environment and other factors. The export or re-export of hardware or software containing encryption may be regulated by the U.S. Department of Commerce, Bureau of Industry and Security (for more information, visit www.bis.doc.gov), and controlled for import and use outside of the U.S. Seagate reserves the right to change, without notice, product offerings or specifications.

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# **Support**

For Internal SSD Support, visit: https://www.seagate.com/support/products/

For Firmware Download and Tools Download for Secure Erase, visit: https://www.seagate.com/support/downloads/

For information regarding online support and services, visit: http://www.seagate.com/contacts/

For information regarding Warranty Support, visit: http://www.seagate.com/support/warranty-and-replacements/

For information regarding data recovery services, visit:

http://www.seagate.com/services-software/seagate-recovery-services/recover/

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# 1. Introduction

The Seagate® FireCuda® 530R SSD is a versatile NVMe SSD with PCle Gen4 x4 interface. It is up to 13x faster than SATA SSDs and delivers ultra-fast performance and enhanced endurance for long term use.

Table 1 The FireCuda 530R SSD Features

Feature	Description		
Capacity (User)	■ 1000GB, 2000GB, 4000GB		
Certifications, Eco-Compliance	■ CE, UL, FCC, BSMI, KCC, Microsoft WHQL, VCCI, CB ■ RoHS		
Dimensions	1000GB  Length, Max 80.15mm  Width, Max 22.15mm  Height, Max 2.23mm  2000GB, 4000GB  Length, Max 80.15mm  Width, Max 22.15mm  Height, Max 3.58mm  With heatsink  Length, Max 80.85mm  Width, Max 24.22mm  Height, Max 11.10mm		
Form Factor	■ M2 2280-S2-M ■ M2 2280-D2-M		
Weight	■ 1000GB: 7.3g ■ 2000GB: 8.8g ■ 4000GB: 9.2g With heatsink ■ 49g (max)		
Endurance	Total Bytes Written ■ 1000GB: 1100TB ■ 2000GB: 2400TB ■ 4000GB: 5050TB	See Section 2.5, Reliability/Endurance.	
Interface Compliance	<ul> <li>NVMe 1.4</li> <li>PCI Express Base 4.0, PCIe Gen 4 x 4 lane, and backward compatible to PCIe Gen3, Gen 2, and Gen 1</li> </ul>		
NAND	■ TLC		
Operating Systems	<ul> <li>Windows 11</li> <li>Windows 10 (64 bit)</li> <li>Ubuntu 20.04, 22.04</li> <li>CentOS 7, 8</li> </ul>		
Performance Random	<ul><li>Read: Up to 1,300,000 IOPS</li><li>Write: Up to 1,300,000 IOPS</li></ul>	Actual performance might vary depending on use conditions and environment.  See Section 2.2, <i>Performance</i> .	
Performance Sequential	■ Read: Up to 7400MB/s ■ Write: Up to 7000MB/s	Actual performance might vary depending on the capacity, use conditions and environment. See Section 2.2, <i>Performance</i> .	

Table 1 The FireCuda 530R SSD Features (continued)

Feature	Description			
Power Consumption  ■ Active Power, Average: <8.9W ■ Idle Power PS3, Average: <29mW ■ Low Power L1.2 mode: < 5mW		See Section 2.3, Power Consumption.		
Power Management	<ul> <li>Supports ActiveStatePower Management (ASPM)</li> <li>Supports Autonomous Power StateTransition (APST)</li> <li>Supports L1.2</li> </ul>			
Reliability	iability ■ End-to-end data path protection ■ MTBF: 1.8 million hours ■ UBER: 1 error in 10 <sup>16</sup> bits read			
Shock and Vibration	Shock ■ Non-Operating: 1,500 Gs, at 0.5 ms  Vibration ■ Non-Operating: 1.52 Gs <sub>RMS</sub> , (20 to 80 Hz, Frequency)	See Section 2.4, Environmental Conditions.		
Temperature ■ Operating: 0°C to 70°C  Range ■ Non-operating: -40°C to 85°C				
Voltage	■ Min = 3.14V±5% ■ Max = 3.47V±5%			
Warranty	Five years, or when the device reaches Host TBW, whichever happens first. Endurance rating valid for SSD Life Remaining > 1%.			

#### 1.1 References

In case of conflict between this document and the following reference documents, this document takes precedence.

- PCle Specifications
  - PCIe PCI Express Electromechanical specification, revision 4.0
  - NVMe Non Volatile Memory Express specification 1.4
  - PCIe CEM PCI Express Card Electromechanical specification, revision 1.1
  - PCI Express M.2 Specification, revision 1.1
- Seagate Downloads are available on the Seagate Support page here: https://www.seagate.com/support/

# 2. Specifications

## 2.1 Models and Capacity

Table 2 Models and Capacity

User Capacity	Standard Models	Heatsink Models	
1000GB	ZP1000GM30063	ZP1000GM30073	
2000GB	ZP2000GM30063	ZP2000GM30073	
4000GB	ZP4000GM30063	ZP4000GM30073	

#### **NOTE** About capacity:

- Sector Size: 512 Bytes (default) and 4K
- User-addressable LBA count = (97696368) + (1953504 x (Desired Capacity in Gb-50.0)) From International Disk Drive Equipment and Materials Association (IDEMA) (*LBA1-03\_standard.doc*)

#### 2.2 Performance

Table 3 Random and Sequential Read and Write Performance

	CrystalDiskMark			
Capacity	Sequential Read (MB/s)	Sequential Write (MB/s)	Random Read (IOPS)	Random Write (IOPS)
1000GB	7400	7000	1,300,000	1,300,000
2000GB	7400	6900	1,300,000	1,300,000
4000GB	7400	6800	1,300,000	1,300,000

#### **NOTE** About performance:

- Fresh out of box (FOB) performance obtained on newly formatted drive.
   Performance may vary based on the SSD's firmware version, system hardware, and configuration.
- Performance is based on AMD Gen4 X570 + AMD Ryzen7 5800X + 8 Core CPU + 16 GB of DDR4 (3200mHz).
- Performance is measured with the following conditions:
  - Sequential: CrystalDiskMark 7.0.0, 1GB range, QD=16, Thread=1 (Default Affinity enabled)
  - Random: CrystalDiskMark 7.0.0, 1GB range, QD=32, Thread=16 (Default Affinity enabled)

# 2.3 Power Consumption

**Table 4 Power Consumption** 

	Power Consumption			
Capacity	Max Avg Read (W)	Max Avg Write (W)	Idle PS3 (mW)	L1.2 (mW)
1000GB	7.8	8.0	25	5
2000GB	8.0	8.2	30	5
4000GB	8.5	8.7	35	5

#### **NOTE** About power consumption:

- The average value of power consumption is based on 100% conversion efficiency.
- Power consumption measured during sequential read and sequential write while running CrystalDiskMark 6.0.2 x64 1GB Range, QD128, Thread=1.
- The measured power voltage is 3.3 V.
- Measured under ambient temperature.
- Power Consumption can differ with configuration and platform.
- Power Consumption during read and write operation is measured at M.2 slot that nearest CPU on Gen4 X570 + AMD Ryzen 7 5800X 8-Core CPU.

#### 2.4 Environmental Conditions

Table 5 Temperature, Humidity, Shock

Specification	Value	
Temperature		
Operating (case temperature at specific airflow)	0°C to 70°C	
Non-operating	-40°C to 85°C	
Humidity		
Operating	90%	
Non-operating (storage)	93%	
Shock		
Non-operating	1,500 G, duration 0.5 ms	
Vibration		
Non-operating	1.52 G <sub>RMS</sub> , (20Hz to 80Hz, Frequency)	

#### **NOTE**

**Temperature** is measured without condensation. Operating mode temperature is measured by temperature sensor, SMART Attribute.

**Airflow** is suggested. Airflow allows the device to be operated at the appropriate temperature for each component during heavy workloads environments.

**Shock and vibration** results assume that the SSD is mounted securely with the input vibration applied to the SSD mounting. These specifications do not cover connection issues that may result from testing at this level. The measured specification is in root mean square (RMS) form.

- Non-operating Shock. The limits of non-operating shock applies to all conditions of handling and transportation. This includes both isolated SSD and integrated SSDs. Shock may be applied in the X, Y, or Z-axis.
- **Non-Operating Vibration.** The limits of non-operating vibration shall apply to all conditions of handling and transportation. This includes both isolated SSD and integrated SSDs. Vibration may be applied in the X, Y, or Z-axis.

# 2.5 Reliability/Endurance

Table 6 Reliability/Endurance

Specification	Value	
Mean time between failures (MTBF)	1.8 million hours	
Bit Error Rate	1 error in 10 <sup>16</sup> bits read	
Endurance	Total Bytes Written	
	■ 1000GB: 1100TB	
	■ 2000GB: 2400TB	
	■ 4000GB: 5050TB	

#### **NOTE** About endurance:

- The SSD achieves the specified MTBF in an operational environment that complies with the operational temperature range specified in this manual. Operating temperatures are measured by temperature sensor.
- Endurance rating valid for SSD Life Remaining > 1%.

# 3. Mechanical Dimensions and Drawings

This section includes, weight, dimensions, and mechanical drawings.

Table 7 FireCuda 530R SSD Weight and Dimensions

Capacity	Weight (g)	Length (Max)	Width (Max)	Height (Max)	
1000GB	7.3	80.15mm			2.23mm
2000GB	8.8		22.15mm	3.58 mm	
4000GB	9.2			3.36 111111	

Table 8 FireCuda 530R SSD with Heatsink Weight and Dimensions

Capacity	Weight (g)	Length (Max)	Width (Max)	Height (Max)
1000GB	49.0			9.84mm
2000GB	49.0	80.85mm	24.22	11.10mm
4000GB	49.0			11.10mm

Figure 1 FireCuda 530R SSD M2 2280-D2-M Top View (2000GB, 4000GB)

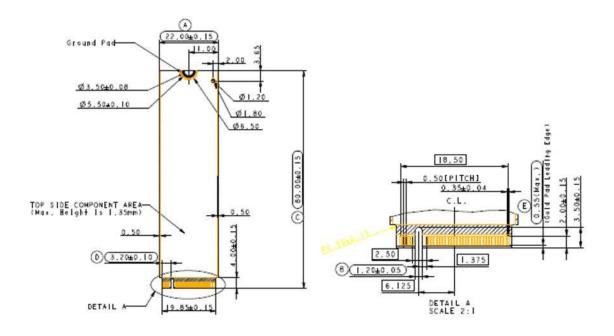


Figure 2 FireCuda 530R SSD M2 2280-D2-M Side View (2000GB, 4000GB)

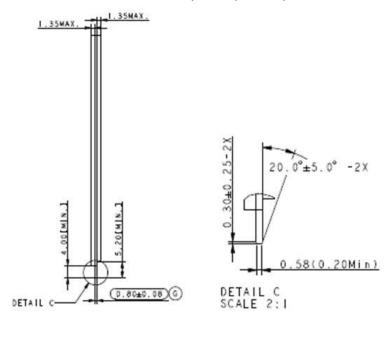


Figure 3 FireCuda 530R SSD M2 2280-D2-M Bottom View (2000GB, 4000GB)

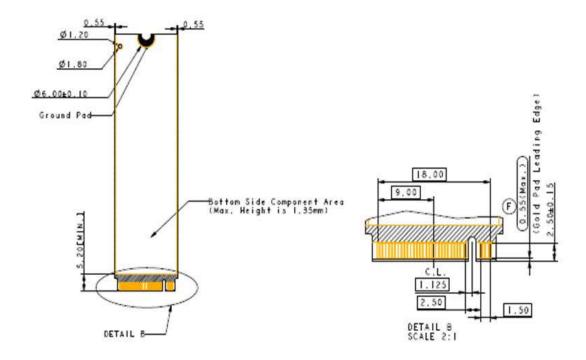


Figure 4 FireCuda 530R SSD M2 2280-S2-M Top View (1000GB)

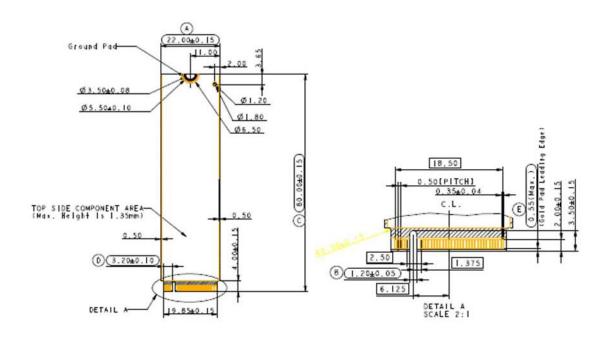


Figure 5 FireCuda 530R SSD M2 2280-S2-M Side View (1000GB)

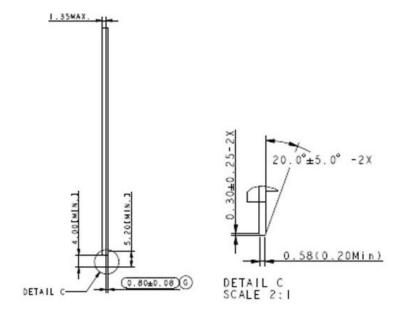


Figure 6 FireCuda 530R SSD M2 2280-S2-M Bottom View (1000GB)

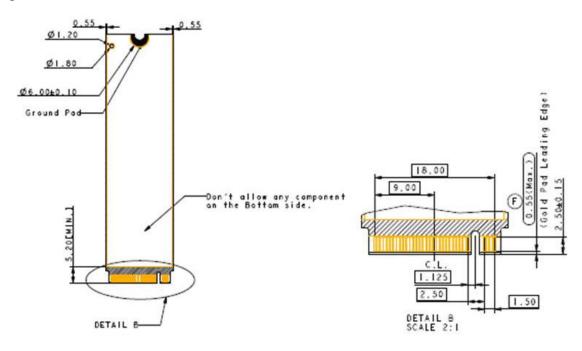
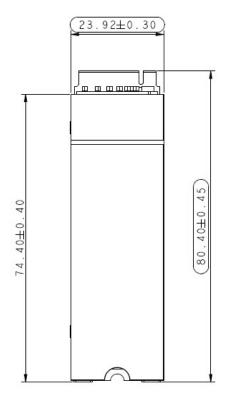
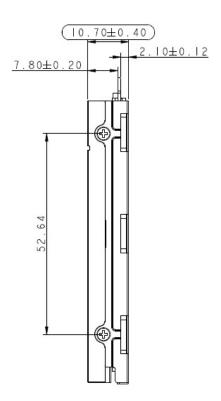


Figure 7 FireCuda 530R SSDs with Heatsink





# 4. Pin and Signal Descriptions

Table 9 Pin Descriptions

Pin No.	PCle Pin	Description	
1	GND	CONFIG_3 = GND	
2	3.3V	3.3V source	
3	GND	Ground	
4	3.3V	3.3V source	
5	PETn3	PCIe TX Differential signal defined by the PCI Express M.2 spec	
6	N/C	No connect	
7	PETp3	PCIe TX Differential signal defined by the PCI Express M.2 spec	
8	N/C	No connect	
9	GND	Ground	
10	LED1#	Open drain, active low signal. These signals are used to allow the add- in card to provide status indicators via LED devices that will be provided by the system.	
11	PERn3	PCIe RX Differential signal defined by the PCI Express M.2 spec	
12	3.3V	3.3V source	
13	PERp3	PCIe RX Differential signal defined by the PCI Express M.2 spec	
14	3.3V	3.3V source	
15	GND	Ground	
16	3.3V	3.3V source	
17	PETn2	PCIe TX Differential signal defined by the PCI Express M.2 spec	
18	3.3V	3.3V source	
19	PETp2	PCIe TX Differential signal defined by the PCI Express M.2 spec	
20	N/C	No connect	
21	GND	Ground	
22	N/C	No connect	
23	PERn2	PCIe RX Differential signal defined by the PCI Express M.2 spec	
24	N/C	No connect	
25	PERp2	PCIe RX Differential signal defined by the PCI Express M.2 spec	
26	N/C	No connect	
27	GND	Ground	
28	N/C	No connect	
29	PETn1	PCIe TX Differential signal defined by the PCI Express M.2 spec	
30	N/C	No connect	

 Table 9 Pin Descriptions (continued)

Pin No.	PCIe Pin	Description	
31	PETp1	PCIe TX Differential signal defined by the PCI Express M.2 spec	
32	N/C	No connect	
33	GND	Ground	
34	N/C	No connect	
35	PERn1	PCIe RX Differential signal defined by the PCI Express M.2 spec	
36	N/C	No connect	
37	PERp1	PCIe RX Differential signal defined by the PCI Express M.2 spec	
38	N/C	No connect	
39	GND	Ground	
40	SMB_CLK (I/O)(0/1.8V)	SMBus Clock; Open Drain with pull-up on platform	
41	PETn0	PCIe TX Differential signal defined by the PCI Express M.2 spec	
42	SMB_DATA (I/O)(0/1.8V)	SMBus Data; Open Drain with pull-up on platform.	
43	PETp0	PCIe TX Differential signal defined by the PCI Express M.2 spec	
44	ALERT#(O) (0/1.8V)	Alert notification to master; Open Drain with pull-up on platform; Active low.	
45	GND	Ground	
46	N/C	No connect	
47	PERn0	PCIe RX Differential signal defined by the PCI Express M.2 spec	
48	N/C	No connect	
49	PERp0	PCIe RX Differential signal defined by the PCI Express M.2 spec	
50	PERST#(I)(0/3.3V)	PE-Reset is a functional reset to the card as defined by the PCle Mini CEM specification.	
51	GND	Ground	
52	CLKREQ#(I/O)(0/3.3V)	Clock Request is a reference clock request signal as defined by the PCIe Mini CEM specification; Also used by L1 PM Sub-states.	
53	REFCLKn	PCIe Reference Clock signals (100 MHz) defined by the PCI Express M.2 spec.	
54	PEWAKE#(I/O)(0/3.3V)	PCIe PME Wake.  Open Drain with pull up on platform; Active Low.	
55	REFCLKp	PCIe Reference Clock signals (100 MHz) defined by the PCI Express M.2 spec.	
56	Reserved for MFG DATA	Manufacturing Data line. Used for SSD manufacturing only. Not used in normal operation. Pins should be left N/C in platform Socket.	

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Table 9 Pin Descriptions (continued)

Pin No.	PCle Pin	Description	
57	GND	Ground	
58	Reserved for MFG CLOCK	Manufacturing Clock line. Used for SSD manufacturing only.  Not used in normal operation.  Pins should be left N/C in platform Socket.	
59	Module Key M		
60	Module Key M		
61	Module Key M		
62	Module Key M	— Module Key	
63	Module Key M	iviodule key	
64	Module Key M		
65	Module Key M		
66	Module Key M		
67	N/C	No connect	
68	SUSCLK(32KHz) (I)(0/3.3V)	32.768 kHz clock supply input that is provided by the platform chipset to reduce power and cost for the module.	
69	N/C	PEDET (NC-PCIe)	
70	3.3V	3.3V source	
71	GND	Ground	
72	3.3V	3.3V source	
73	GND	Ground	
74	3.3V	3.3V source	
75	GND	Ground	

# 5. NVMe Commands

**Table 10 Admin Commands** 

Identifier	O/M	Command Description	Supported
00h	М	Delete I/O Submission Queue	Supported
01h	М	Create I/O Submission Queue	Supported
02h	М	Get Log Page	Supported
04h	М	Delete I/O Completion Queue	Supported
05h	М	Create I/O Completion Queue	Supported
06h	М	Identify	Supported
08h	М	Abort	Supported
09h	М	Set Feature	Supported
0Ah	М	Get Feature	Supported
0Ch	М	Asynchronous Event Request	Supported
10h	0	Firmware Commit	Supported
11h	0	Firmware Image Download	Supported
14h	0	Device Self-test	Supported
80h	0	Format NVM	Supported
81h	0	Security Send	Supported
82h	0	Security Receive	Supported
84h	0	Sanitize	Supported

Table 11 I/O Commands

Identifier	O/M	Command Description	Supported
00h	0	Flush	Supported
01h	0	Write	Supported
02h	0	Read	Supported
04h	0	Write Uncorrectable	Not Supported
05h	0	Compare	Supported
08h	0	Write Zeroes	Supported
09h	0	Dataset Management	Supported

**Table 12 Set Feature Commands** 

Identifier	O/M	Command Description	Supported
00h		Reserved	
01h	М	Arbitration	Supported
02h	М	Power Management	Supported
03h	0	LBA Range Type	Not Supported
04h	М	Temperature Threshold	Supported

**Table 12 Set Feature Commands** 

05h	М	Error Recovery	Supported
06h	0	Volatile Write Cache	Supported
07h	М	Number Of Queues	Supported
08h	М	Interrupt Coalescing	Supported
09h	М	Interrupt Vector Configuration	Supported
0Ah	М	Write Atomicity Normal	Supported
0Bh	М	Asynchronous Event Configuration	Supported
0Ch	0	Autonomous Power State Transition	Supported
0Dh	0	Host Memory Buffer	Not Supported
0Eh	0	Timestamp Supported	
10h	0	Host Controlled Thermal Management Supported	
11h	0	Non-Operational Power State Config Supported	
0Eh - 7Dh		Reserved	
80h	0	Software Progress Marker Supported	

Table 13 Get Log Page Commands

Identifier	O/M	Command Description	Supported
00h		Reserved	
01h	М	Error Information	Supported
02h	М	SMART / Health Information	Supported
03h	М	Firmware Slot Information	Supported
04h	0	Changed Namespace List Supported	
06h	0	evice Self-test Supported	
09h - 7Fh		Reserved	
81h	0	Sanitize Status	Supported
82h - FFh		Reserved	

# 6. SMART Support

The FireCuda 530R SSD supports the SMART command set.

## **6.1 SMART Attributes**

The following table lists SMART Attributes and Descriptions.

Table 14 SMART Attributes (Log Identifier 02h)

Bytes Index	Bytes	Description	
[0]	1	Critical Warning	
[2:1]	2	Composite Temperature	
[3]	1	Available Spare	
[4]	1	Available Spare Threshold	
[5]	1	Percentage Used	
[31:6]	26	Reserved	
[47:32]	16	Data Units Read	
[63:48]	16	Data Units Written	
[79:64]	16	Host Read Commands	
[95:80]	16	Host Write Commands	
[111:96]	16	Controller Busy Time	
[127:112]	16	Power Cycles	
[143:128]	16	Power On Hours	
[159:144]	16	Unsafe Shutdowns	
[175:160]	16	Media and Data Integrity Errors	
[191:176]	16	Number of Error Information Log Entries	
[195:192]	4	Warning Composite Temperature Time	
[199:196]	4	Critical Composite Temperature Time	
[201:200]	2	Temperature Sensor 1 (Current Temperature)	
[203:202]	2	Temperature Sensor 2 (N/A)	
[205:204]	2	Temperature Sensor 3 (N/A)	
[207:206]	2	Temperature Sensor 4 (N/A)	
[209:208]	2	Temperature Sensor 5 (N/A)	
[211:210]	2	Temperature Sensor 6 (N/A)	
[213:212]	2	Temperature Sensor 7 (N/A)	
[215:214]	2	Temperature Sensor 8 (N/A)	
[511:216]	296	Reserved	

#### NOTES

For (Log Identifier 02h:

- "Critical Warning [Byte 0]"
- This field indicates critical warnings for the state of the controller.
  - Bit#0: Available spare is below threshold
  - Bit#1: Temperature exceeded threshold or below an under temperature threshold
  - Bit#2: Reliability is degraded due to excessive media or internal errors
  - Bit#3: Media is placed in read only mode
  - BIt#4: Volatile memory backup device has failed.
  - Bit#5 Bit#7: Reserved
- "Available Spare [Byte 3]" Value (percentage) = 100\* [(total reserved VB consumed VB caused by early, later bad)/ total reserved VB]
- "Percentage Used [Byte 5]"
- Value (percentage) = 100\* (total VB erase count/ PE cycle for total VB)

Table 15 SMART Attributes (Log Identifier C0h)

Bytes Index	Bytes	Description
[7:0]	8	Device Capacity
[15:8]	8	User Capacity
[23:16]	8	NAND Read
[31:24]	8	NAND Write
[39:32]	8	NAND Erase Sector
[47:40]	8	SSD Life Remaining Percent D3
[55:48]	8	SSD Life Used Percent D3
[56]	1	WP Water Mark
[58:57]	2	Highest temperature
[62:59]	4	Flash UNC Error Count
[67:63]	5	Data E3D Error
[70:67]	4	PHY Error Count
[74:71]	4	Total Bad Block Count
[78:75]	4	Total Early Bad Block Count
[82:79]	4	Total Later Bad Block Count
[86:83]	4	Read Fail Count
[90:87]	4	Program Fail Count
[94:91]	4	Erase Failure Count
[102:95]	8	System Table Copy Count
[110:103]	8	Read Move Table Count
[114:111]	4	Data read retry count
[118:115]	4	RAID ECC retry count
[122:119]	4	RAID ECC failed count
[130:123]	8	Total Erase Count
[134:131]	4	Max Erase Count
[138:135]	4	Average Erase Count
[142:139]	4	Min Erase Count
[150:143]	8	Background read count
[154:151]	4	Host Write Uncorrectable Sector Count
[158:155]	4	PS3 Enter Success
[162:159]	4	PS4 Enter Success
[166:163]	4	Wear Leveling Count
[167]	1	Chip internal temperature
[169:168]	2	Thermal throttling
[171:170]	2	Thermal throttling time
[179:172]	8	FW Code Update Count
[511:181]	331	RSV

#### **NOTES**

For Log Identifier C0h:

- "SSD Life Remaining Percent D3 [Byte 47:40]"
  - Value (percentage) = 100 \*[1 (Average of the Flash's block erase count / NAND EOL erase count)]
- "SSD Life Used Percent [Byte 55:48]"
  - Value (percentage) = 100 \*(Average of the Flash's block erase count / NAND EOL erase count)

### 7. Feature Details

## 7.1 Flash Management

#### 7.1.1 Error Correction Code (ECC)

Flash memory cells will deteriorate with use, which might generate random bit errors in the stored data. Thus, FireCuda 530R SSD applies the fourth generation LDPC(Low Density Parity Check) of ECC algorithm, which can detect and correct errors that occur during read process, ensure data has been read correctly, as well as protect data from corruption.

#### 7.1.2 Wear Leveling

NAND flash devices can only undergo a limited number of program/erase cycles, and in most cases, the flash media are not used evenly. If some areas get updated more frequently than others, the lifetime of the device would be reduced significantly. Thus, Wear Leveling is applied to extend the lifespan of NAND Flash by evenly distributing write and erase cycles across the media.

Seagate provides advanced Wear Leveling algorithm, which can efficiently spread out the flash usage through the whole flash media area. Moreover, by implementing both dynamic and static Wear Leveling algorithms, the life expectancy of the NAND flash is greatly improved.

### 7.1.3 Bad Block Management

Bad blocks are blocks that do not function properly or contain more invalid bits causing stored data to become unstable, and their reliability is not guaranteed. Blocks that are identified and marked as bad by the manufacturer are referred to as "Early Bad Blocks". Bad blocks that are developed during the lifespan of the flash are named "Later Bad Blocks". Seagate implements an efficient bad block management algorithm to detect the factory- produced bad blocks and manages bad blocks that appear with use. This practice prevents data being stored into bad blocks and further improves the data reliability.

#### 7.1.4 TRIM

TRIM is a feature which helps improve the read/write performance and speed of solid-state drives (SSD). Unlike hard disk drives (HDD), SSDs are not able to overwrite existing data, so the available space gradually becomes smaller with each use. With the TRIM command, the operating system can inform the SSD which blocks of data are no longer in use and can be removed permanently. Thus, the SSD will perform the erase action, which prevents unused data from occupying blocks all the time.

#### 7.1.5 **SMART**

SMART, an acronym for Self-Monitoring, Analysis and Reporting Technology, is an open standard that allows a hard disk drive to automatically detect its health and report potential failures. When a failure is recorded by SMART, users can choose to replace the drive to prevent unexpected outage or data loss. Moreover, SMART can inform users of impending failures while there is still time to perform proactive actions, such as copy data to another device.

#### 7.1.6 Over Provisioning

Over Provisioning refers to the inclusion of extra NAND capacity in a SSD, which is not visible and cannot be used by users. With Over Provisioning, the performance and IOPS (Input/output Operations per Second) are improved by providing the controller additional space to manage P/E cycles, which enhances the reliability and endurance as well. Moreover, the write amplification of the SSD becomes lower when the controller writes data to the flash.

#### 7.1.7 Firmware Upgrade

Firmware can be considered as a set of instructions on how the device communicates with the host. Firmware will be upgraded when new features are added, compatibility issues are fixed, or read/write performance gets improved.

#### 7.1.8 Thermal Throttling

The purpose of thermal throttling is to prevent any components in a SSD from over-heating during read and write operations. The device is designed with an on-die and an on-board thermal sensor, and with its accuracy, firmware can apply different levels of throttling to achieve the purpose of protection efficiently and proactively via SMART reading.

Table 16 Current version: Thermal Throttling 2.0

Item	Content
SMART reporting temperature	Flash normalized case temperature
Reference of temp. reading	Flash on-die thermal sensor and Controller on-die thermal sensor
tmt1 threshold	CTL Tj = 113°C or NAND = 81°C per SMART reported
tmt2 threshold	CTL Tj = 118°C or NAND = 83°C per SMART reported
Protect threshold	85°C per Smart reported
Protect controller threshold	CTL = 118°C
Fatal threshold	CTL_Fatal = 125°C or NAND_Fatal = 88°C
Resume performance threshold	CTL Tj = 103°C or NAND = 77°C per SMART reported
Temperature polling frequency	Every 1 sec
TMT1_state impact	APU add delay between both read and write
TMT2_state impact	APU add delay between both read and write

#### NOTE

For optimal performance:

Provide sufficient airflow and cooling.

## 7.2 Advanced Device Security Features

#### 7.2.1 NVMe format

Secure Erase is a standard NVMe format command and it writes all "0xFF" to fully wipe all the data on the SSDs. When this command is issued, the SSD controller erases its storage blocks and returns the drive to its factory default settings.

#### 7.2.2 Sanitize Operation

The Sanitize feature is an alternative to the existing secure erase capabilities through the Format NVM command and makes a robust data security by ensuring the user data from the drive's media, caches and the Controller Memory Buffer are all wiped by the block erase operations, overwriting or destroying the encryption key. The following table illustrates the types of Sanitize Operations supported.

**Table 17 Supported Sanitize Operations** 

Drive Security Type	Sanitize Operation		
	Overwrite	Block Erase	Crypto Erase
Non-SED	Yes	Yes	No

NOTE

Sanitize Overwrite command completion takes at least one hour per terabyte per pass. The number of passes is drive-selectable. The NVMe spec default is 16 passes. Contact Seagate Support for more detailed information.

## 7.3 SSD Lifetime Management

#### 7.3.1 Total Bytes Written (TBW)

TBW (total bytes written) is a measurement of the SSDs' expected lifespan, which represents the amount of data written to the device. To calculate the TBW of a SSD, the following equation is applied:

TBW = [(NAND Endurance) x (SSD Capacity)] / WAF

NAND Endurance: NAND endurance refers to the P/E (Program/Erase) cycle of a NAND flash. SSD Capacity: The SSD capacity is the specific capacity in total of a SSD.

WAF: Write Amplification Factor (WAF) is a numerical value representing the ratio between the amount of data that a SSD controller needs to write and the amount of data that the host's flash controller writes. A better WAF, which is near 1, guarantees better endurance and lower frequency of data written to flash memory.

#### 7.3.2 Media Wear Indicator

Actual life indicator reported by SMART Attribute byte index [5], Percentage Used, recommends User to replace drive when reaching to 100%.

#### 7.3.3 Read Only Mode (End of Life)

When drive is aged by cumulated program/erase cycles, media worn- out may cause increasing numbers of later bad block. When the number of available spare is less the threshold(5%, SMART attribute log ID 02h Byte4), the drive will notify Host through AER event and Critical Warning to enter Read Only Mode to prevent further data corruption. User should start to replace the drive with another one immediately.

## 7.4 An Adaptive Approach to Performance Tuning

### 7.4.1 Throughput

Based on the available space of the disk, the drive will regulate the read/write speed and manage the performance of throughput. When there still remains a lot of space, the firmware will continuously perform read/write action. There is still no need to implement garbage collection to allocate and release memory, which will accelerate the read/write processing to improve the performance. Contrarily, when the space is being used up, the drive will slow down the read/write processing, and implement garbage collection to release memory. Hence, read/write performance will become slower.

#### 7.4.2 Predict & Fetch

Normally, when the Host tries to read data from the PCle SSD, the PCle SSD will only perform one read action after receiving one command. However, the drive applies Predict & Fetch to improve the read speed. When the host issues sequential read commands to the PCle SSD, the PCle SSD will automatically expect that the following will also be read commands. Thus, before receiving the next command, flash has already prepared the data. Accordingly, this accelerates the data processing time, and the host does not need to wait so long to receive data.

#### 7.4.3 SLC Caching

The firmware design of the device currently adopts dynamic caching to deliver better performance for better endurance and consumer user experience. The SLC caching size is up to 1/3 of free capacity of the SSD.

# 8. Safety, Standards, and Compliance

Each Hard Drive and Solid State Drive ("device") has a product label that includes certifications that apply to that specific drive. The following information provides an overview of requirements that may apply to the drive.

NOTE

The most up to date information on Safety, Standards, and Compliance for this product is available in the Seagate HDD and SSD Regulatory Compliance and Safety document. You can find this document on the Seagate Support page here:

https://www.seagate.com/support/

# 8.1 Regulatory Model Numbers

The following regulatory model number represents all features and configurations in the series:

■ STA046

## 9. FireCuda 530R Installation Precautions

### 9.1 FireCuda 530R SSD Handling Instructions

There are a lot of components assembled on a single SSD device. Handle the drive with care especially when it has any WLCSP (Wafer Level Chip Scale Packaging) components such as PMIC, thermal sensor or load switch. WLCSP is a packaging technology widely used for making smaller footprints. However, any bumps or scratches may damage those ultrasmall parts so you must handle with gentle care. See Figure 9.2, FireCuda 530R SSD Installation Instructions

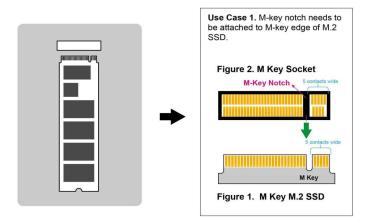
CAUTION! DO NOT DROP SSD

CAUTION! INSTALL SSD WITH CARE

CAUTION! STORE SSD IN A PROPER PACKAGE

#### 9.2 FireCuda 530R SSD Installation Instructions

FireCuda 530R features the PCIe connector with M-key, which is compatible only with the M-key socket. See Use Case 1 in Figure 8, M-Key M.2 Assembly Precautions. As shown in Use Case 2, misuse may cause severe damage to an SSD including burn-out.



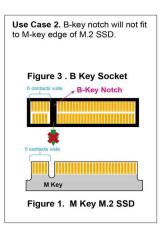


Figure 8 M-Key M.2 Assembly Precautions

#### 9.3 Heatsink Disclosures

**NOTE** About Heatsink SSDs.

- Heatsinks are pre-installed. Do not remove the heatsink; you can damage the SSD.
- Dimensions exceed standard size specifications for the M.2 without a heatsink. Please verify that your system has enough space for installation.
- SSDs with heatsinks are recommended for M.2 with connectors H3.2 and above.



#### **Seagate Technology LLC**